

**REMARKS**

Applicant respectfully asserts that the foregoing amendments should be entered as the amendments are not being submitted for purpose of delay, do not involve new matter, and are directed to form, not substance, and place the application in better form for publication. Additionally, Applicant respectfully requests that the below substitutions be made to clarify the reasons for allowance, and that the Information Disclosure Statement be initialed and returned.

**I. Comments on Statement of Reasons For Allowance**

In the Office Action mailed on August 24, 2004, the following was an Examiner's statement of reasons for allowance under section 6 of that Office Action:

The above claims are allowed because the prior art of record fails to disclose or suggest a reverse mode transistor configured to protect a circuit between a pad and ground; and having a capacitance between the base and the emitter of about ten times less than the parasitic capacitance between the collector and the substrate.

Applicant respectfully requests that the word "pad" in the Examiner's statement of reasons for allowance be replaced with the word "source," as described in claim 1.

In the Notice of Allowability (Part of Paper No./Mail Date 20040917), the following is an Examiner's statement of reasons for allowance as detailed under section 2 of page 2:

Regarding claims 13-16, 18, 20, 22-28, 30: These are allowable because the prior art of record fails to disclose or suggest, in combination with the other claim limitations; a bipolar junction transistor with the base coupled to a resistance element and the bipolar transistor configured with a breakdown voltage less than the breakdown voltage of the circuit to be protected.

Applicant respectfully requests that the above examiner's statement of reasons for allowance regarding claims 13-16, 18, 20, 22-28, and 30 be substituted as follows:

Regarding claims 13-16, these claims are allowable because the prior art of record fails to disclose or suggest, in combination with other claim

limitations, a bipolar junction transistor with the base coupled to a resistance element.

With regard to claims 20, 22-28, and 30, these claims are allowable because the prior art of record fails to disclose or suggest, in combination with the other claim limitations, a bipolar junction transistor configured with a breakdown voltage less than the breakdown voltage of the circuit to be protected.

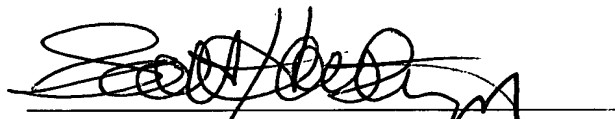
**II. Information Disclosure Statement**

Per Applicant's attorney's voice mail messages left with Examiner Demakis on 10/27/04 and 10/29/04, Applicant respectfully requests that the Examiner initial all applicable spaces on the IDS form PTO-1449 (a copy enclosed for your convenience, labeled as Exhibit A) submitted on December 15, 2000, as evidenced by the USPTO acknowledge receipt (see Exhibit B), in order to indicate full consideration of all references identified therein.

***CONCLUSION***

Applicant respectfully asserts that the foregoing amendments are not being submitted for purpose of delay and do not involve new matter. If, in the opinion of the Examiner, a telephonic conference would expedite entry of the foregoing amendment, the Examiner is invited to call the undersigned attorney at (770) 933-9500.

Respectfully submitted,

  
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Scott A. Horstemeyer; Reg. No. 34,183

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HORSTEMEYER & RISLEY, L.L.P.**  
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## **Exhibit A**

### **IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Larry B. Li	Docket No.:	50944.9900
Serial No.:	Not yet assigned	Client Ref:	98RSS266
Filing Date:	Filed herewith	Group Art Unit:	Not yet assigned
Title:	ELECTROSTATIC DISCHARGE PROTECTION FOR HIGH FREQUENCY CIRCUITS	Examiner:	Not yet assigned

### **INFORMATION DISCLOSURE STATEMENT**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Assistant Commissioner:

This Information Disclosure Statement is being filed in accordance with the duty of disclosure under 37 C.F.R. § 1.56 and pursuant to 37 C.F.R. §§ 1.97-1.98. Copies of the cited references are being submitted herewith. The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant reserves the right to dispute any of the listed documents as prior art during examination. Further, Applicant does not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application. Furthermore, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made or that no other material information may exist.

It is respectfully requested that these documents be considered by the examiner and an initialed copy of each form be returned to the undersigned.

This Information Disclosure Statement is being filed in compliance with 37 C.F.R. § 1.97(b)(1) as it is being filed within three months of the filing date of the Patent Application. Therefore, there is no fee required.

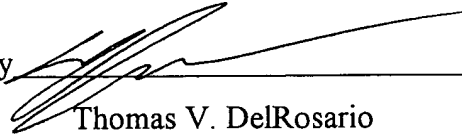
The Assistant Commissioner is hereby authorized to charge Deposit Account No. 19-2814 for any deficiency in the fees required for the filing of this Information Disclosure

Statement, for which purpose a duplicate copy of this sheet is enclosed. **This statement does not authorize the payment of the issue fee.**

Respectfully submitted,

Date: December 15, 2000

By



Thomas V. DelRosario  
Reg. No. 46,658

Enclosures: PTO-1449  
cited references

**SNELL & WILMER, L.L.P.**  
One Arizona Center  
400 East Van Buren  
Phoenix, AZ 85004-2202  
(602) 382-6219

<b>INFORMATION DISCLOSURE CITATION</b> (Use several sheets if necessary)				Docket Number (Optional) <b>50944.990</b>		Application Number <b>Not Yet Assigned</b>	
				Applicant(s) <b>Larry Baoqi Li</b>			
				Filing Date <b>Filed Herewith</b>		Group Art Unit <b>NYA</b>	

U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		5,781,389	7/14/1998	Fukazako et al.	361	56	
		5,969,929	10/19/1999	Kleveland et al.	361	111	
		5,173,755	12/22/1992	Co et al.	257	361	
		5,838,146	11/17/1998	Singer	323	270	
		5,631,793	5/20/1997	Ker et al.	361	56	
		5,936,284	8/10/1999	Ravanelli	257	361	

FOREIGN PATENT DOCUMENTS								
	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
		EP 0 435 047 A2	7/3/1991	Eurpoe	H01L	27/02		

OTHER DOCUMENTS <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>	
	Chatterjee, A Low Voltage Triggering SCR for ON-Chip ESD Protection at Output and Input Pads, IEEE Electron Device Letters, Vol. 12, No. 1, January 1991, pages 21-22
	Duvvury, Rountree, & Adams, Internal Chip ESD Phenomena Beyond the Protection Circuit, IEEE Transactions on Electron Devices, Vol. 35, No. 12, December 1988, pages 2133-2138

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

# Exhibit B

THE U.S. PATENT AND TRADEMARK OFFICE OFFICIAL MAIL  
ROOM STAMP AFFIXED HERETO ACKNOWLEDGES RECEIPT OF  
THE ITEMS CHECKED BELOW:

Serial No.: TBA  
Applicant: LARRY B. LI  
Filing Date: DEC. 15, 2000  
Title/Mark: ELECTROSTATIC DISCHARGE  
PROTECTION CIRCUIT

- ☒ Patent Application  
☒ 11 Pages in Spec 19 No of Claims  
☒ Drawing Sheets: (F) 1 (INF.)  
☒ Check \$ 710.00 No.: 491382  
☐ Power of Attorney  
☐ Extension of Time (duplicate)  
☐ Preliminary Amendment  
☐ Amendment

jection  
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THE U.S. PATENT AND TRADEMARK OFFICE OFFICIAL MAIL  
ROOM STAMP AFFIXED HERETO ACKNOWLEDGES RECEIPT OF  
THE ITEMS CHECKED BELOW:

Serial No.: TBA  
Applicant: LARRY B. LI  
Filing Date: DEC. 15, 2000  
Title/Mark: ELECTROSTATIC DISCHARGE  
PROTECTION CIRCUIT

Fee

Class Mail on

ss Mail No.

DEC. 15, 2000

Atty: TVD

- ☒ Patent Application  
☒ 11 Pages in Spec 19 No of Claims  
☒ Drawing Sheets: (F) 1 (INF.)  
☒ Check \$ 710.00 No.: 491382  
☐ Power of Attorney  
☐ Extension of Time (duplicate)  
☐ Preliminary Amendment  
☐ Amendment  
☐ Amendment after FINAL Rejection  
☐ Issue Fee (Base and/or Balance)  
☐ Small Entity Certificate  
☐ Assignment, Coversheet and Fee  
☐ Trademark Application  
☒ IDS



**426614982US**

☐ Marked items placed in First Class Mail on

☒ Marked items filed via Express Mail No.

EL426614982US on DEC. 15, 2000

S&W Docket No.: 50944.9900 Atty: TVD